

<b>100% FIDELITY</b>	<b>IMAGE</b>
<b>SUBCLASS</b>	<b>ISSUE CLASSIFICATION</b>
<b>CLASS</b>	

PATENT NUMBER

**U.S. UTILITY Patent Application**

O.I.P.E.

PATENT DATE

7/1/1998

SCANNED AS 30A TM

APPLICATION NO. 09/772027	CONT/PRIOR F	CLASS 226 712	SUBCLASS 226 226	ART UNIT 2183 2183	EXAMINER Trent H. Coope
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## APPLICANTS

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## TITLE

Logic integrated circuit, and recording medium readable by a computer, which stores a source of CPU core on said logic integrated circuit

PTO-2040  
12/99**ISSUING CLASSIFICATION**

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets	Dwg.	Figs.	Dwg.	Print Fig.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE FEE	
				Amount Due	Date Paid
				ISSUE BATCH NUMBER	

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